

FIG. 1

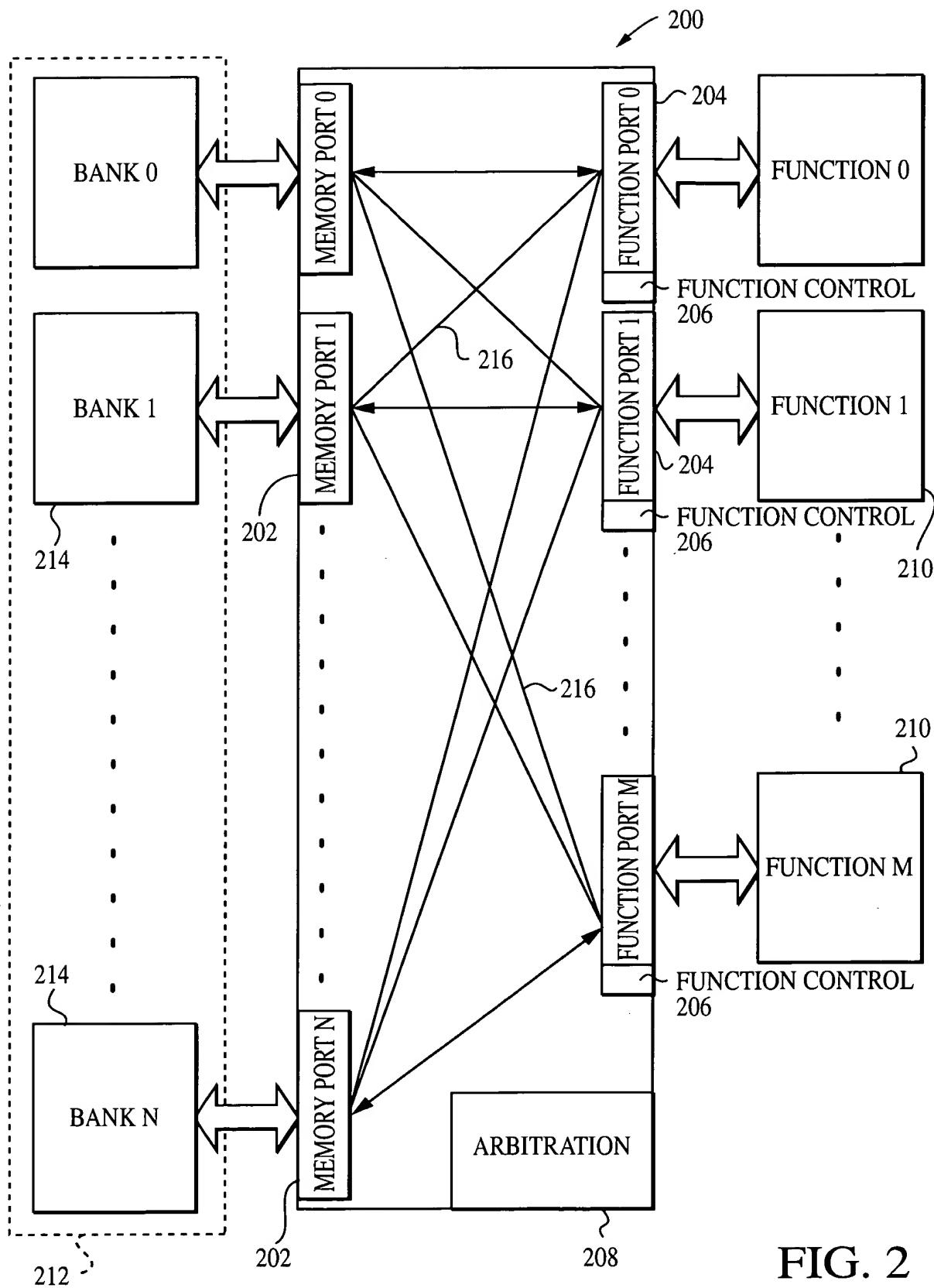


FIG. 2

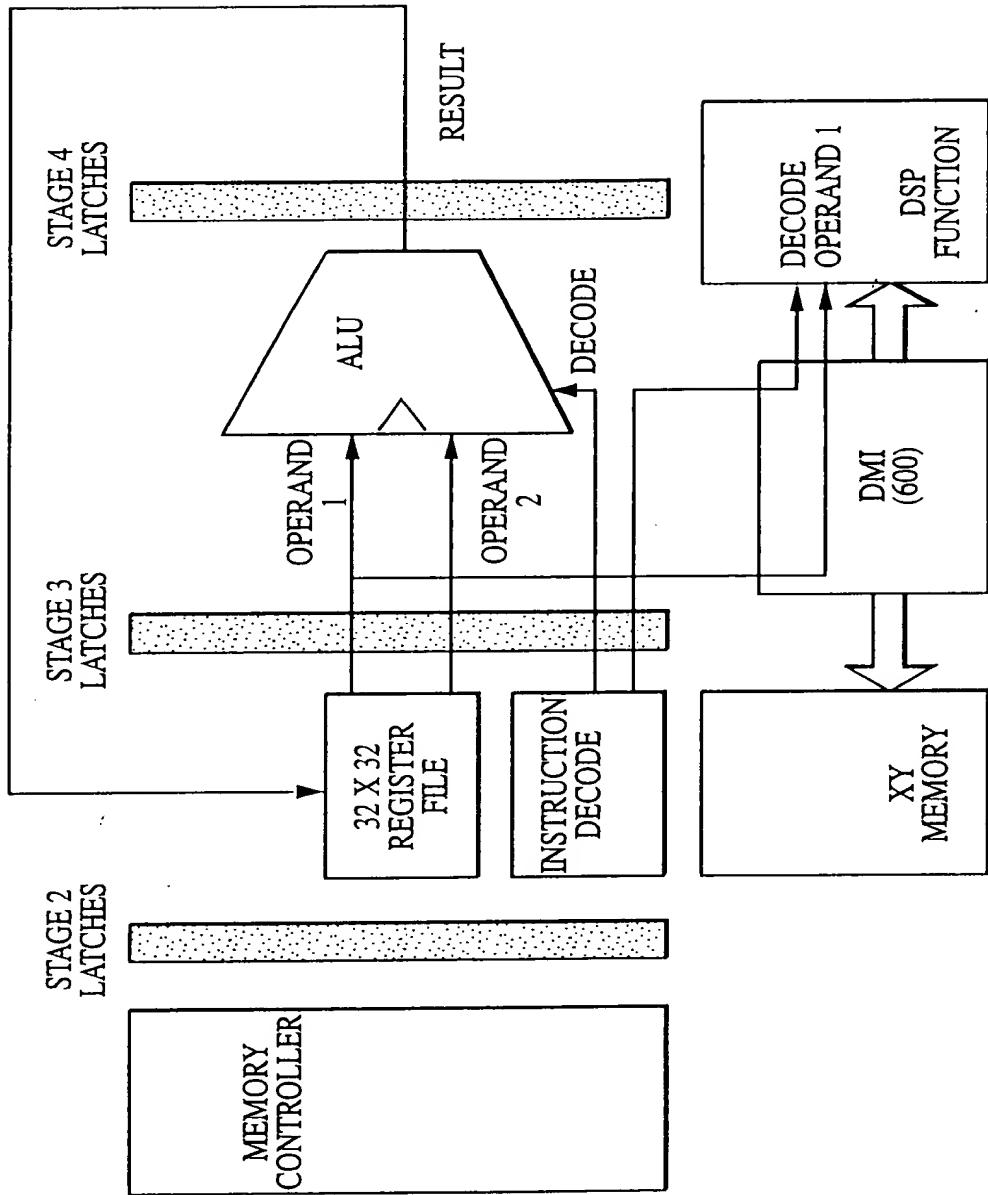


FIG. 2a

10232000-TR2000000

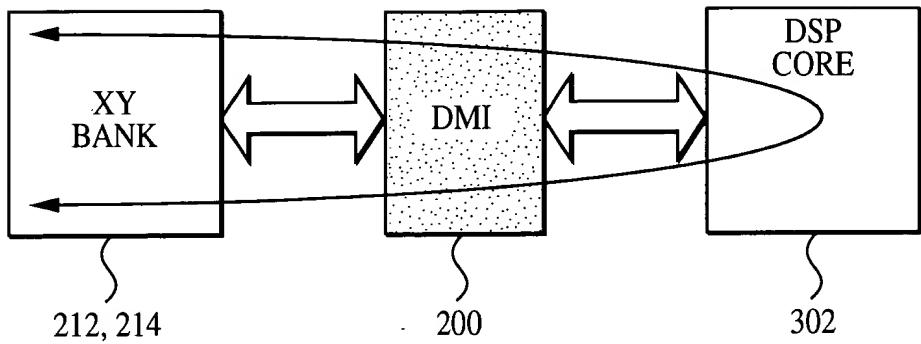


FIG. 3

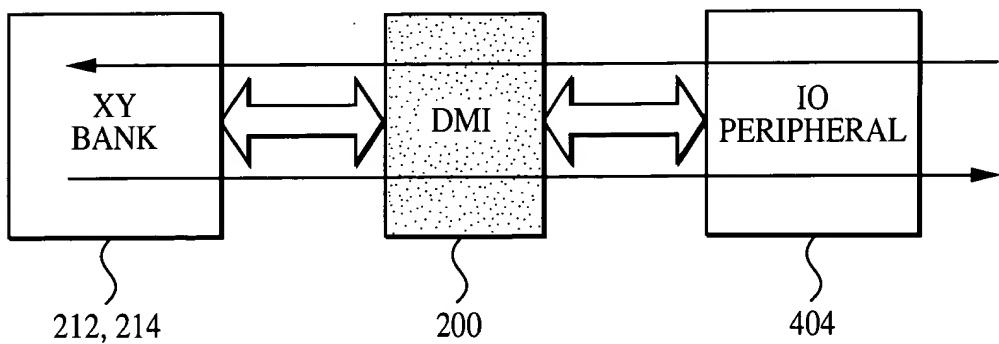


FIG. 5

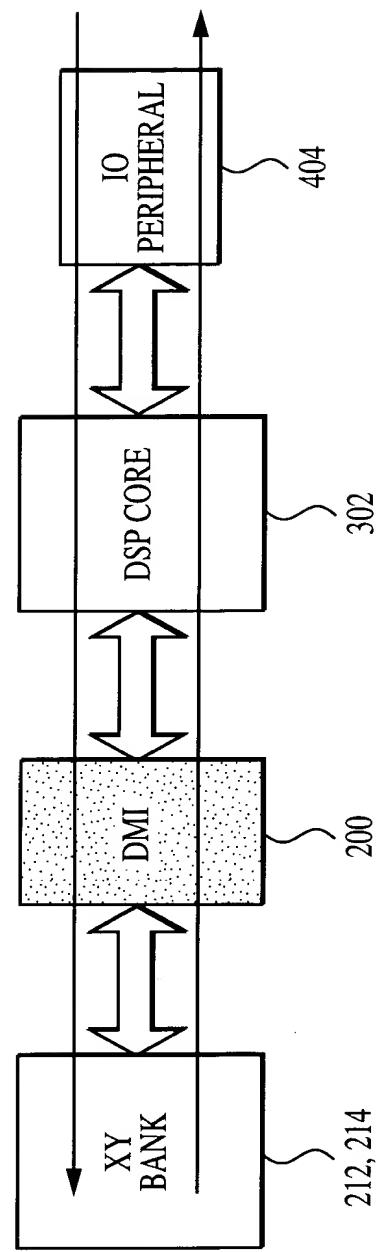


FIG. 4

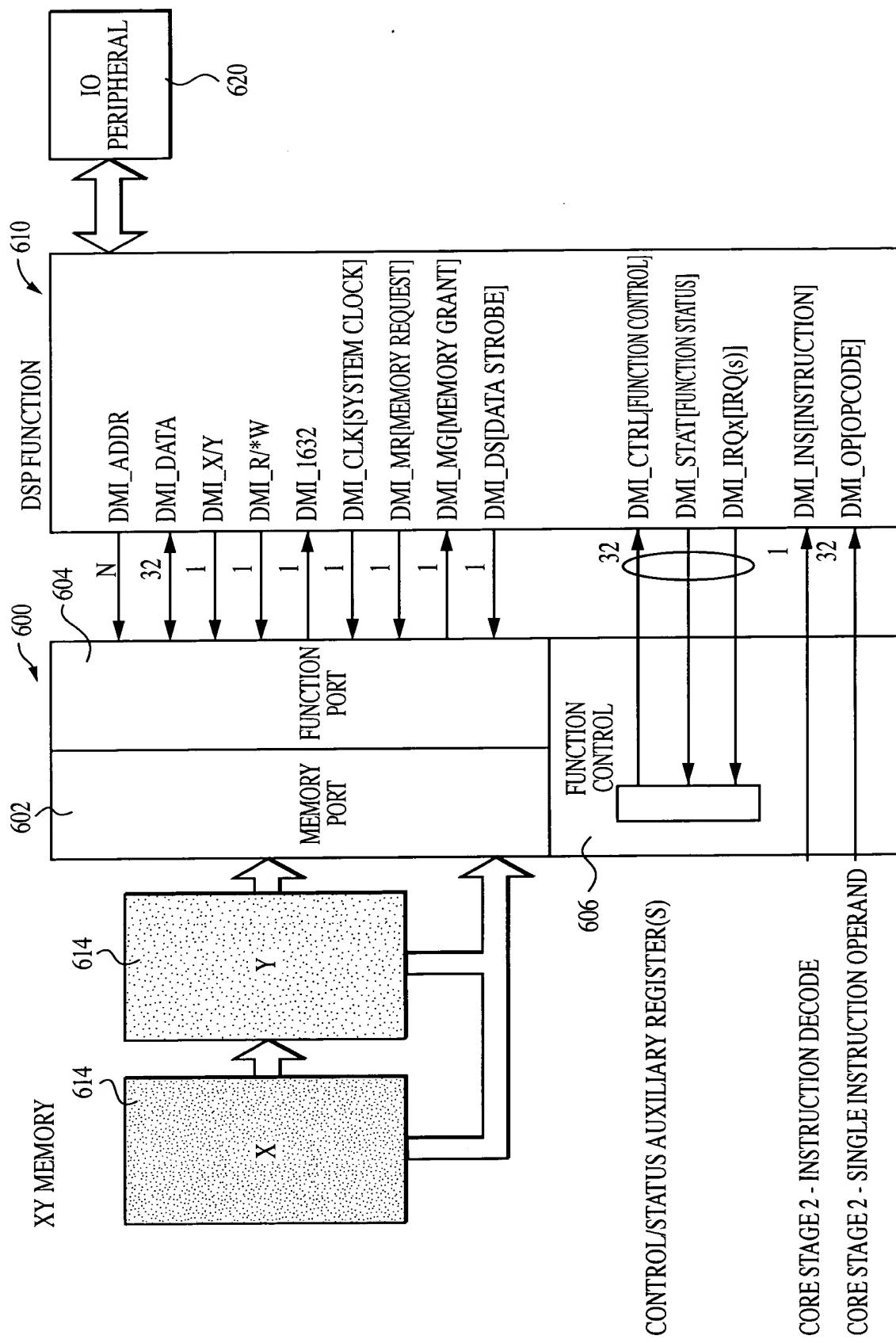


FIG. 6

FIG. 6a

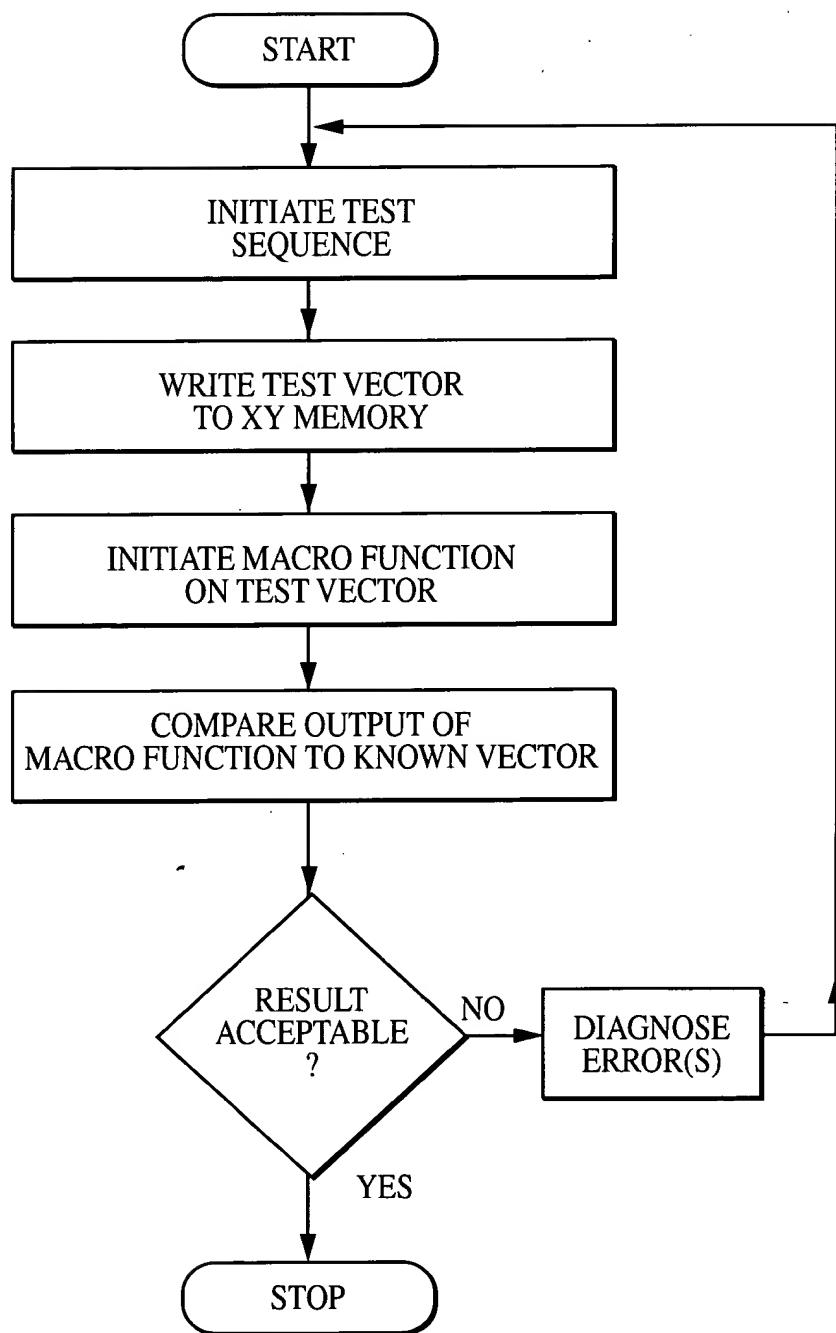


FIG. 7. DMI Timing Diagram

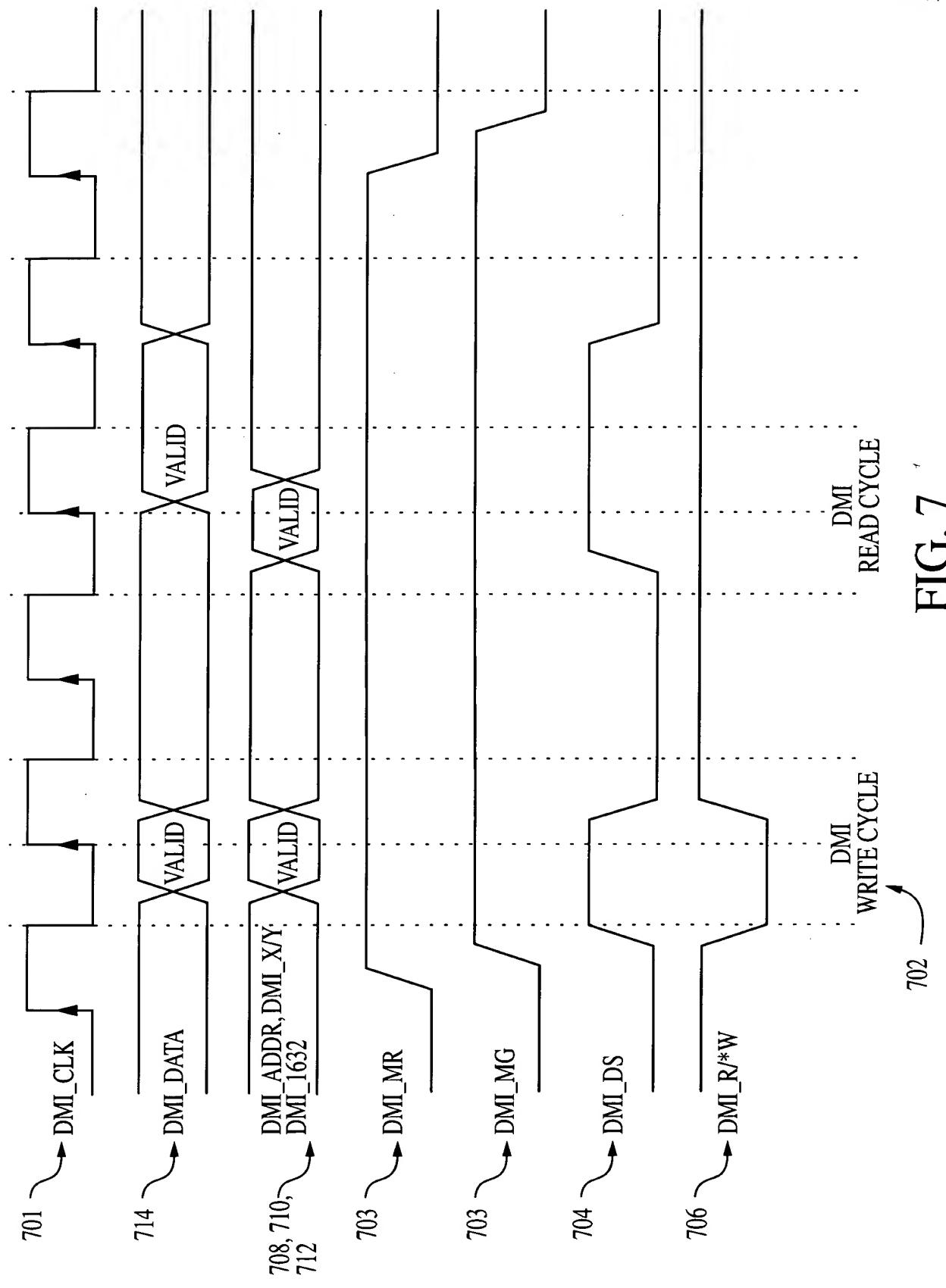


FIG. 7

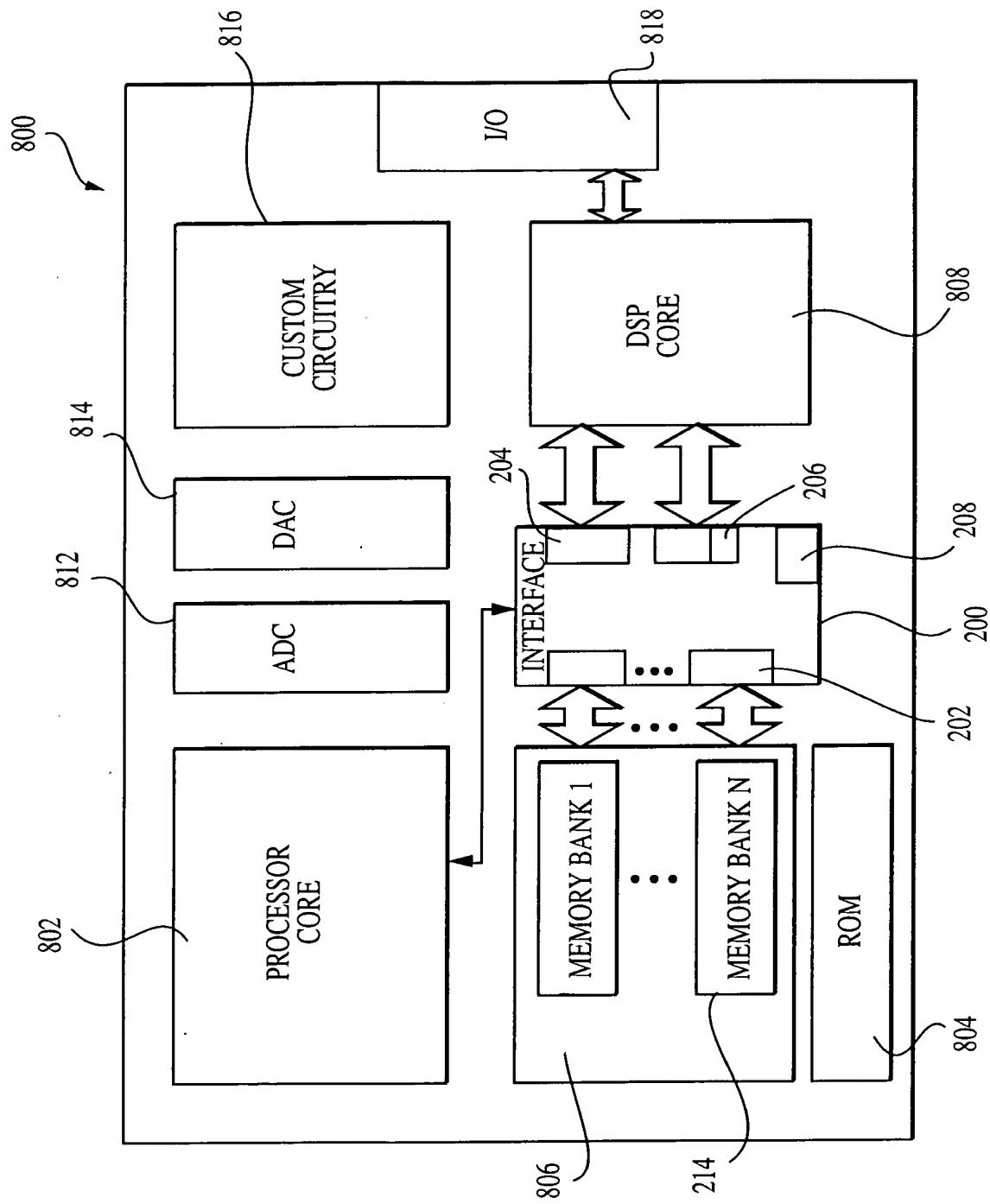
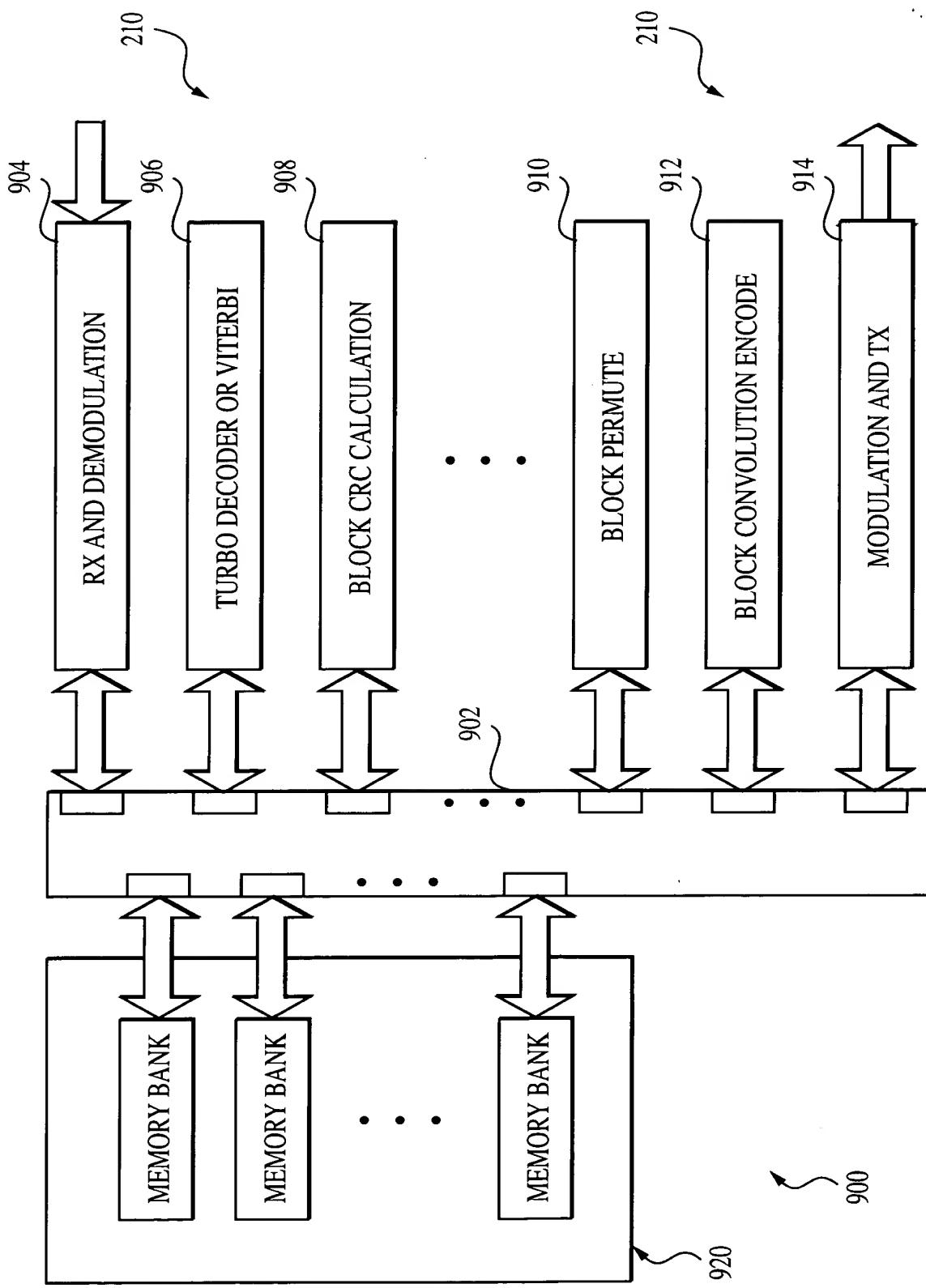


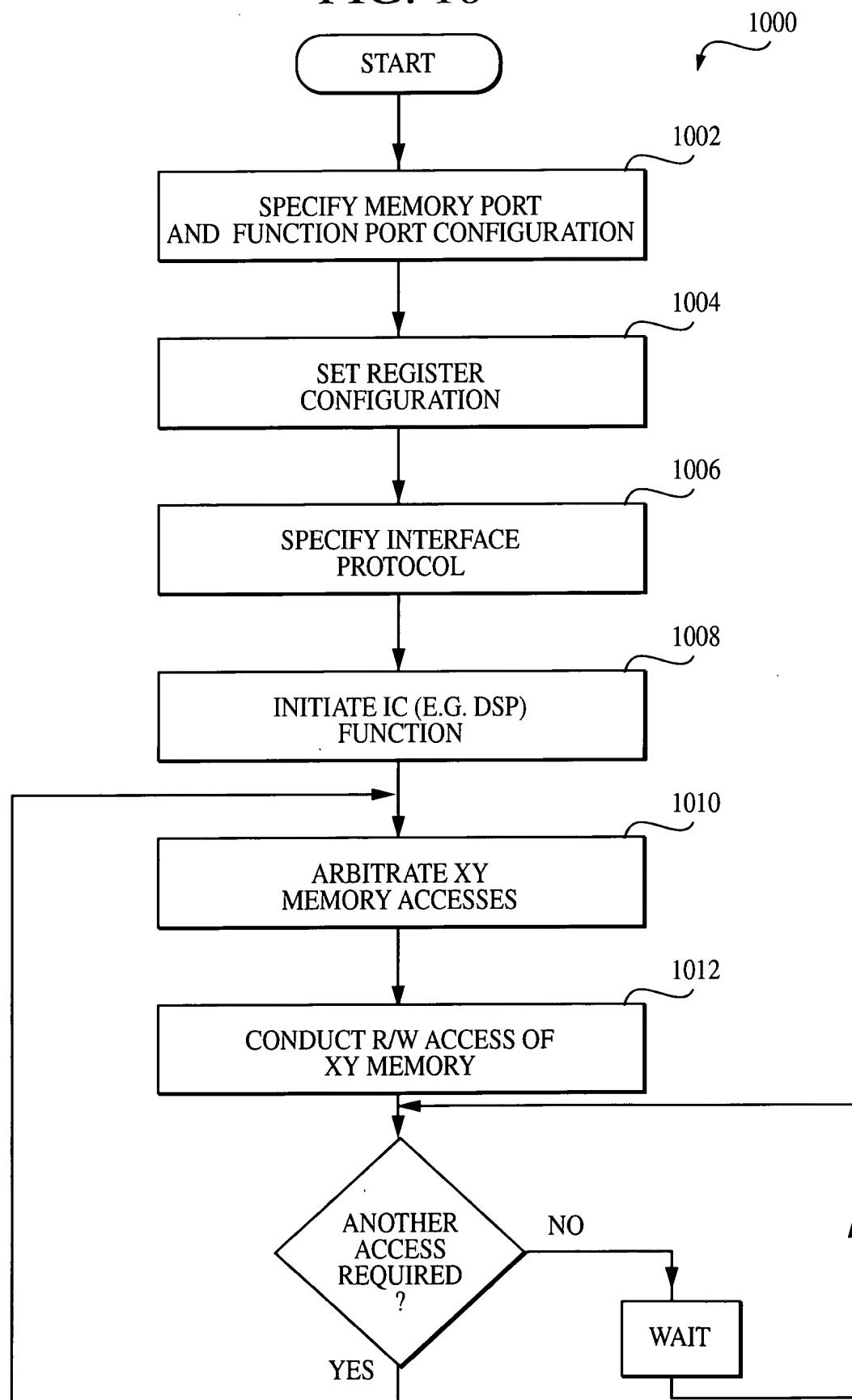
FIG. 8



	Data Block 0	Data Block 1	Data Block 2	Data Block 3	Data Block 4	...
Bank 0	Function 0	Function 1	Function 2	Function 3	Function 0	
Bank 1	-	Function 0	Function 1	Function 2	Function 3	
Bank 2	-	-	Function 0	Function 1	Function 2	
Bank 3	-	-	-	Function 0	Function 1	

FIG. 9a

FIG. 10



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FIG. 11

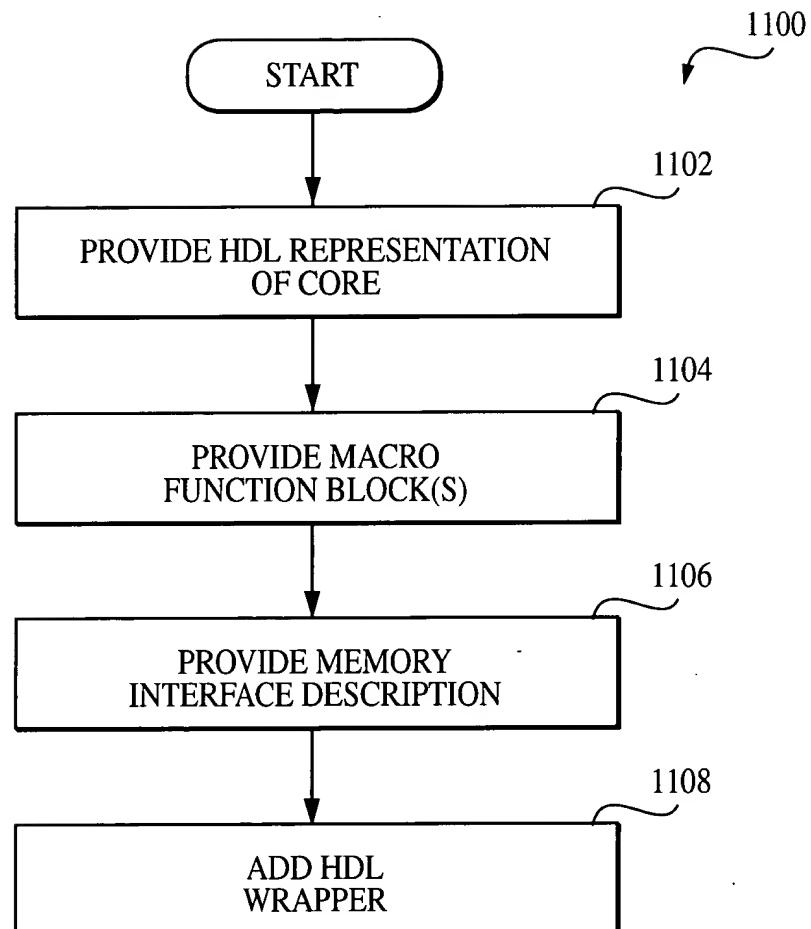


FIG. 11a

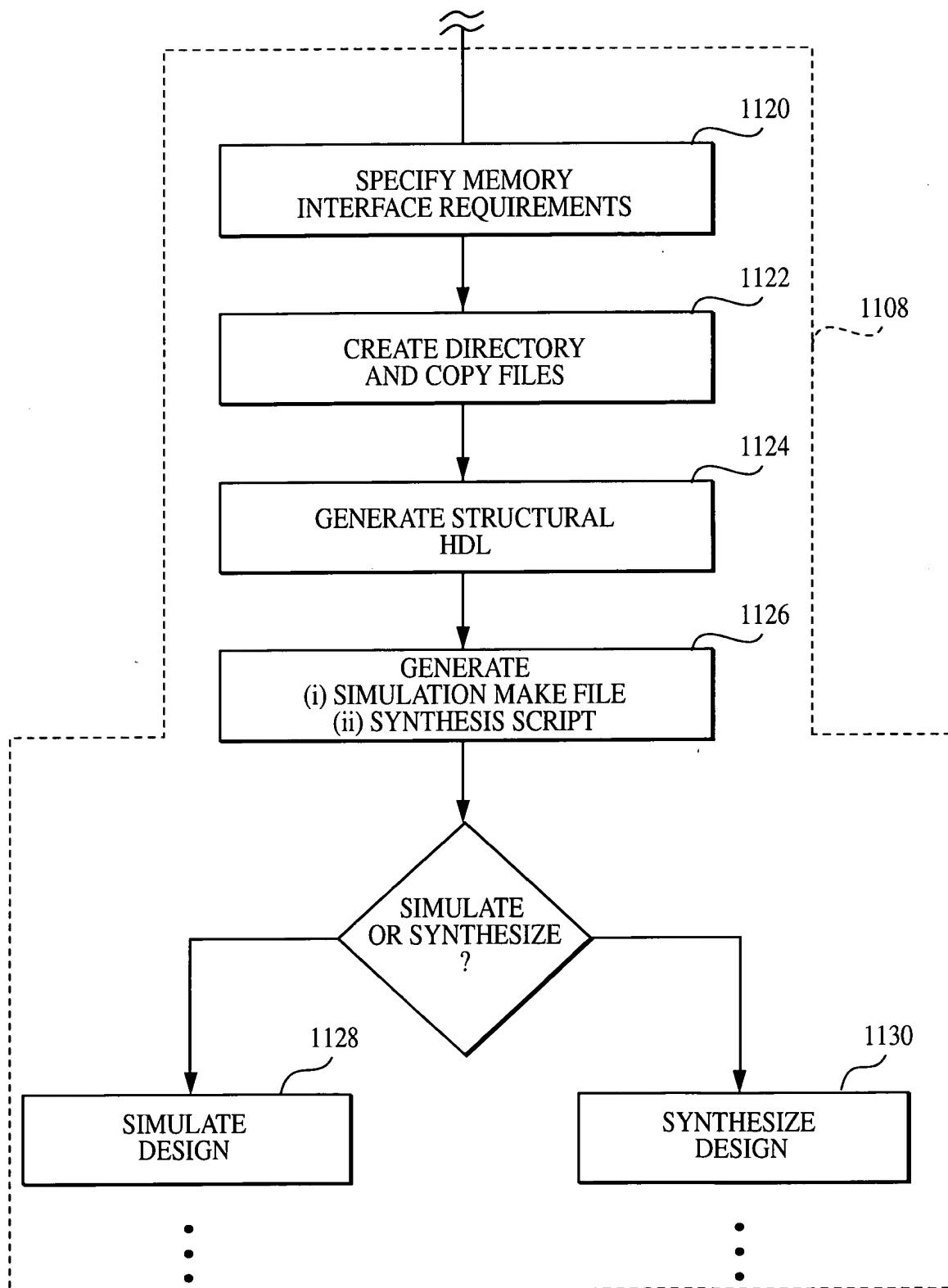


FIG. 12

